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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Walter H. Whitlock
Serial No.: 10/643,597
For: PROCESS AND SYSTEM FOR CLEANING SURFACES OF SEMICONDUCTOR WAFERS
Filed: August 19, 2003
Examiner: Not Yet Assigned
Art Unit: 1746
Confirmation No.: 3946
Customer No.: 27623
Attorney Docket: 386.7819USU

COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, Virginia 22313-1450

Dear Sir:

STATUS REQUEST

The above-identified patent application was filed on August 19, 2003. On March 23, 2004, an Updated Filing Receipt was received. Please advise when we can expect a response.

The Commissioner is hereby authorized to charge any additional fees under 37 C.F.R. §§1.16 and 1.17 which may be required with this communication, or credit any overpayment, to **Deposit Account No. 01-0467**. A duplicate copy of this Form is enclosed.

Respectfully submitted,

Dated: February 2, 2005

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CERTIFICATE OF MAILING

I HEREBY CERTIFY THAT THIS CORRESPONDENCE IS BEING DEPOSITED WITH THE U.S. POSTAL SERVICE AS FIRST CLASS MAIL IN AN ENVELOPE ADDRESSED TO: COMMISSIONER FOR PATENTS, P.O. BOX 1450, ALEXANDRIA, VIRGINIA 22313-1450, ON February 2, 2005.

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2/2/05
DATE